

**LIST OF CURRENT CLAIMS**

1. (Currently Amended) A basic input/output system updating method comprising the steps of:

dividing a memory in an electronic device into a system program area and a boot program area,

storing a basic input/output system (BIOS) program in said system program area such that when a microprocessor of said electronic device reads said program, said microprocessor executes a system initialization sequence of said electronic device;

storing an initialization sequence into said boot program area, wherein when said system program area is destroyed and the BIOS program is lost, said microprocessor, connectable to said boot program area via a switch, enables reading of said initialization sequence in said boot program area[.]; and

wherein said microprocessor during said initialization sequence, activates an integrated drive electronics (IDE) interface of said electronic device, reads a BIOS program stored in said IDE-interface connected to a hard disk drive, and records said BIOS read from said IDE-interface into said system program area.

2. (Previously Presented) The basic input/output system updating method according to Claim 1, wherein said microprocessor, during said initialization sequence, activates the hard disk drive connected to said electronic device, reads said BIOS program stored in said hard disk drive, and records said BIOS into said system program area.

3. (Canceled)

4. (Previously Presented) The basic input/output system updating method according to Claim 1, said microprocessor, during said initialization sequence, links with a local area network (LAN), downloads a BIOS program stored in said LAN, and records said BIOS downloaded from said LAN into said system program area.

5. (Previously Presented) The basic input/output system updating method according to Claim 1, wherein said electronic device is a notebook computer.

6. (Previously Presented) The basic input/output system updating method according to Claim 5, wherein said memory is a flash read-only memory (ROM).

7. (Previously Presented) The basic input/output system updating method according to Claim 5, wherein said switch is a manually operated shunting component.

8. (Previously Presented) The basic input/output system updating method according to Claim 5, wherein when the power supply of said electronic device is switched on, said microprocessor issues an address signal via a decoder which is sent after decoding to said system program area and said boot program area to enable said microprocessor to respectively assign the starting position of said system program area and said boot program area.

9. (Previously Presented) The basic input/output system updating method according to Claim 2, wherein said electronic device is a notebook computer.

10. (Currently Amended) The basic input/output system updating method according to Claim 3, wherein the said electronic device is a notebook computer.

11. (Currently Amended) The basic input/output system updating method according to Claim 4, wherein the said electronic device is a notebook computer.